

10Gb/s 10Km CDR SFP+ 1310nm Transceivers

[illegible]

S a

(T = -5 C 70 C a V = 3.14 3.46V)

Parameter	Symbol	Unit	Min	Typ	Max	Note
T a						
N a Wa			1290		1330	
S M S Ra	SMSR	B	30			
S a W (-20 B)	Δ				1	
O a O P	Pa	B	-6		-1	
E Ra	ER	B	6			
T a a D P a	TDP	B			1	1

A a La P POFF B -30

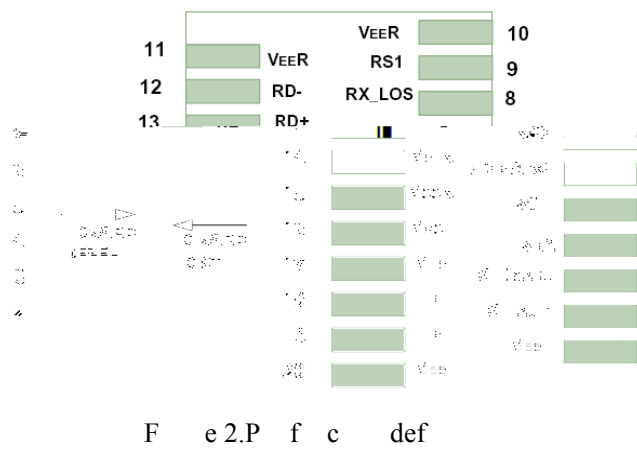
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Electric Ports Definition

Parameter	Symbol	Unit	Min	Typ	Max	Notes
Transceiver						
Transceiver						
Transceiver						
Transceiver						
Transceiver						
Receiver						
Receiver						
Receiver						
Receiver						

Pin function definitions

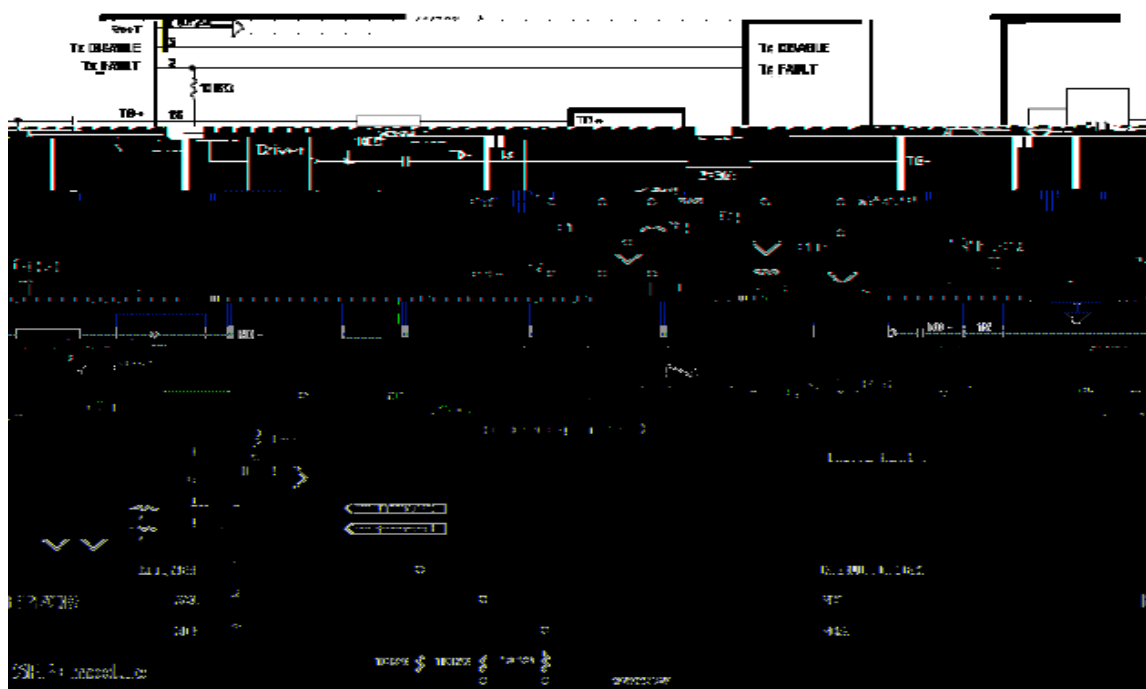


Pin Number	Symbol	Name	Description
1,17,20	VeeT	Transceiver Supply	Transceiver Supply
2	TX Fa	Transceiver Fa (OC)	Transceiver Fa (OC)

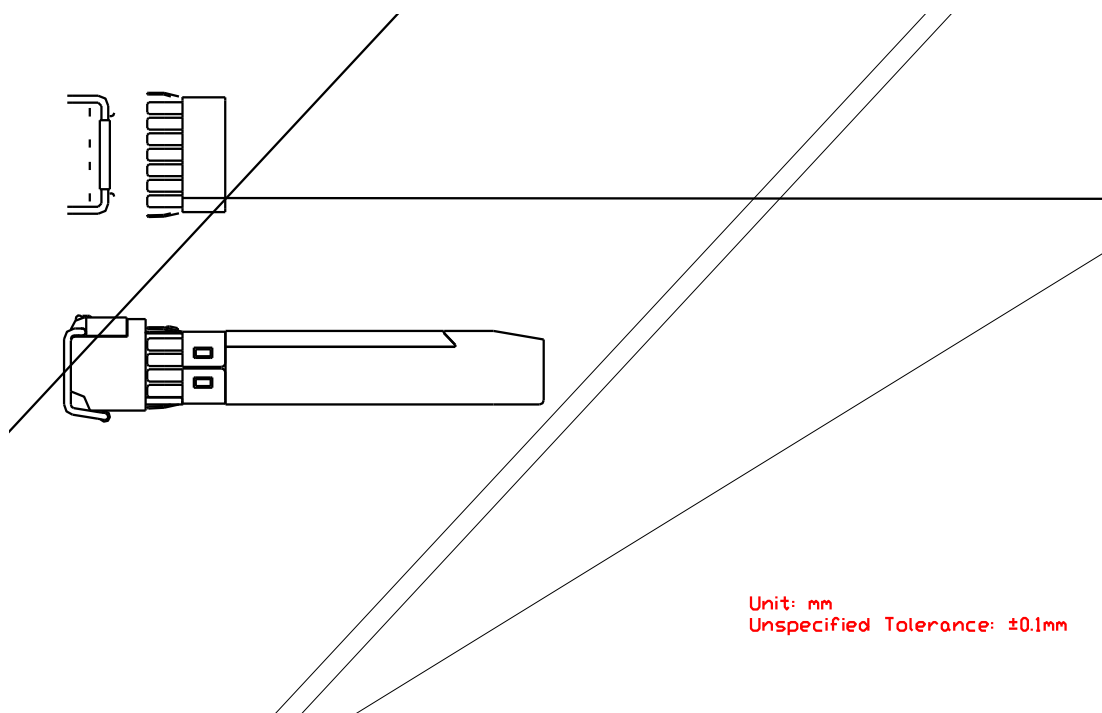
			T	e c ec c a b e, a d	d be ed
			H Vcc	a 10 ! e .	
3	TX D ab e	T a e D ab e I (LVTTL)	T e .	e a ed	VccT a 10 !
4	SDA		Se a ID	SFF 8472 D a c	
5	SCL	M d e Def Ide f e	M d e Def		d be ed H Vcc
6	MOD-ABS		10 ! e .		
7	RS0	Rece e Ra e Se ec (LVTTL)	T e e	a e a e a 30 ! -d	d. A
9	RS1	T a e Ra e Se ec (LVTTL)	a e e f e e		affec d e
8	LOS	L f S a O (OC)	S ff c e ca a f e a BER < 1 10 ⁻¹² = L c 0 I ff c e ca a f e a BER < 1 10 ⁻¹² = L c 1 T e c ec c a b e, a d d be ed H Vcc a 10 ! e .		
10,11,14	VeeR	Rece e S a G d	T e e b a d.	d be c ec ed a d e	
12	RD-	Rece e Ne a e DATA O (CML)	L e a e .	= L c 0 O Rece e DATA AC c ed a d e e e a ed	a 50!
13	RD+	Rece e P e DATA O (CML)	L e a e .	= L c 1 O Rece e DATA AC c ed a d e e e a ed	a 50!
15	VccR	Rece e P e S	T e	d be c ec ed a f e ed +3.3V e e b a d. See F e 3.Rec e ded f e	
16	VccT	T a e P e S	T e	d be c ec ed a f e ed +3.3V e e b a d. See F e 3.Rec e ded f e	
18	TD+	T a e P e DATA I (CML)	L c 1 I e a 100! e .	= L T a e DATA AC c ed a d e a ed a d ffe e a	a e
19	TD-	T a e Ne a e DATA I (CML)	L c 0 I e a 100! e .	= L T a e DATA AC c ed a d e a ed a d ffe e a	a e

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Typical Application Circuit



Package Outline



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Regulatory Compliance

Fea e	Te Me hod	Pe fo mance